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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/784,577	02/23/2004	Donald Thomas McGrath	RD-27645-2 9571 EXAMINER	
6147 GENIEDAL EI	7590 01/24/2008 LECTRIC COMPANY			
GLOBAL RESEARCH PATENT DOCKET RM. BLDG. K1-4A59 NISKAYUNA, NY 12309			SHINGLETON, MICHAEL B	
			ART UNIT	PAPER NUMBER
			2815	
			NOTIFICATION DATE	DELIVERY MODE
			01/24/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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	Application No.	Applicant(s)	
	10/784,577	MCGRATH, DONALD THOMAS	
Office Action Summary	Examiner	Art Unit	
_	Michael B. Shingleton	2815	
The MAILING DATE of this communication app	I		
Period for Reply		·	
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	I. the mailing date of this communication. D (35 U.S.C. § 133).	
Status			
1) ☐ Responsive to communication(s) filed on 16 Sec 2a) ☐ This action is FINAL 2b) ☐ This 3) ☐ Since this application is in condition for alloware closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		
Disposition of Claims			
4) Claim(s) 26-29 is/are pending in the application 4a) Of the above claim(s) 27 is/are withdrawn fr 5) Claim(s) is/are allowed. 6) Claim(s) 26,28 and 29 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or Application Papers	election requirement.		
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the of Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Examiner.	epted or b) objected to by the Edrawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119		•	
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of 	s have been received. s have been received in Application ity documents have been receive (PCT Rule 17.2(a))	on No od in this National Stage	
Attachment(s)			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa	te	

Application/Control Number:

10/784,577 Art Unit: 2815

DETAILED ACTION

The rejections of claims 26, 28 and 29 under USC 103 of the previous office action dated 07-13-2007 are hereby repeated.

Response to Arguments

Applicant's arguments filed 09-19-2007 have been fully considered but they are not persuasive. Applicants argue that the addition of shifting/inverter circuit comprises solely of NMOS depletion mode based devices is not persuasive because the examiner views these arguments as not timely and in any case the use of such has been and continues to be obvious. As stated in the Baird reference is silent on the type of MOSFETs used and applicant has not disputed that these can be made up of solely NMOS depletion mode based devices. As Figure 4 clearly shows of Baird elements like 33 are diode connected FETs and are symbolically shown by the diode symbol between element 33 and 34. When a reference is silent on the type of transistor this is strong evidence that any conventional type can be employed. The type is not critical to the invention. The examiner just cannot see the choice of the type of transistor as presenting a patentable distinction over the prior art. This is akin to choosing the workable range for a device. For example the choice of the particular transistor such that it's parameters of the transistor will work within the workable range of the device. One is just choosing a particular type of transistor that will work with the device. It generally would have been obvious to choose lets say vacuum tubes for the transistor elements of a circuit. But in this case Biard is very specific that depletion mode Fets are to be used and there are only two types i.e. NMOS or PMOS and thus the examiner does not see where Biard excludes NMOS types.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action

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is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael B. Shingleton whose telephone number is (571) 272-1770.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Parker, can be reached on (571) 272-2298. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MBS January 1, 2008

Michael B Shingleton Primary Examiner Group Art Unit 2815